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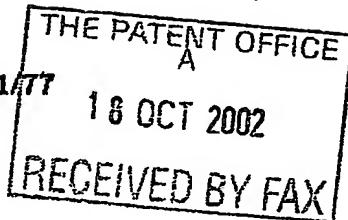
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18 OCT 2002 E756990-1 C63975
P01/7700 0.00-0224297.2

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1. Your reference

02.010-ipw/ph

33

2. Patent application number

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18 OCT 2002

3. Full name, address and postcode of the or of each applicant (underline all surnames)

IPWireless, Inc.
1001 Bayhill Drive, 2nd Floor
San Bruno, California
CA 94068

Patents ADP number (if you know it)

8169401002

If the applicant is a corporate body, give the country/state of its incorporation

USA, Delaware

II

4. Title of the invention

ARRANGEMENT AND METHOD FOR RF FILTER

5. Name of your agent (if you have one)

Peter Hudson

"Address for service" in the United Kingdom to which all correspondence should be sent (including the postcode)

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Patents ADP number (if you know it)

7987431001

II

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Country

Priority application number
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Date of filing
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Number of earlier application

Date of filing
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8. Is a statement of inventorship and of right to grant of a patent required in support of this request? (Answer 'Yes' if:

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11. We request the grant of a patent on the basis of this application.

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Peter D. Hudson

Date

18 October 2002

12. Name and daytime telephone number of person to contact in the United Kingdom

Peter Hudson

01420 562568

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02.010-ipw

- 1 -

DUPLICATE

ARRANGEMENT AND METHOD FOR RF FILTER**Field of the Invention**

5

This invention relates to RF (Radio Frequency) filtering, and particularly (though not exclusively) to such filtering in wireless communication applications.

10

Background of the Invention

Specifications (3GPP TS 25.105 v3.10.0, 'BS Radio Transmission and Reception (TDD)', hereinafter referred 15 to as [1]) by the 3GPP (3rd Generation Partnership Project) set out the performance of TDD (Time Division Duplex) Node B (base station in a 3GPP system) equipment. These specifications cover the 'Adjacent Channel Leakage Ratio' (ACLR) for Node Bs specified for equipment that is 20 co-sited with other TDD or FDD (Frequency Division Duplex) Node Bs operating on adjacent channels.

For co-siting purposes, stringent specifications on the transmit spectral purity of UMTS TDD Node Bs call for a 25 single channel RF filter to be fitted after the power amplifier (PA). The specification of the RF filter is also extremely stringent and a very high Q passive filter is required in order to achieve the required stop-band. By adopting an RF filter with such a steep roll-off 30 factor, it is unavoidable that the filter has an effect on the the transmit accuracy; in fact, the inclusion of

02.010-ipw

- 2 -

this filter may cause the Node B to fail the transmit accuracy requirement.

Passive RF equalisation is not desirable due to the

5 corresponding increase in the complexity of the RF filter and the fact the amplitude equalisation is not possible without increasing the insertion loss across the pass band. Analogue baseband equalisation is not desirable as the equaliser response needs to be matched to the RF

10 filter to achieve optimum transmit accuracy and tuning baseband filters to match the RF filter, which in practice would have a significant impact on the production of the Node B.

15 A need therefore exists for digital pre-equaliser for RF filter wherein the abovementioned disadvantage(s) may be alleviated.

20 **Statement of Invention**

In accordance with a first aspect of the present invention there is provided a filter arrangement, for use in a wireless communication transmitter, as claimed in

25 claim 1.

In accordance with a second aspect of the present invention there is provided a method, for filtering in a wireless communication transmitter, as claimed in claim

30 15.

02.010-ipw

- 3 -

Brief Description of the Drawings

One digital pre-equaliser, arrangement and method for RF
5 filtering incorporating the present invention will now be
described, by way of example only, with reference to the
accompanying drawing(s), in which:

10 FIG. 1 shows a block-diagrammatic representation of
an example transmitter architecture showing
application of digital pre-equalisation;

15 FIG. 2 shows a graphical representation of the
magnitude response of a single channel RF filter;

20 FIG. 3 shows a graphical representation of group
delay improvement following introduction of a phase
equalising digital filter;

25 FIG. 4 shows a graphical representation of an ideal
modulation mask and RF filter amplitude roll-off;

FIG. 5 shows a graphical representation of an
example of amplitude equalized RF filter;

25 FIG. 6 shows a block-diagrammatic representation of
an example implementation of the digital pre-
equalising FIR filter of FIG. 1.

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02.010-ipw

- 4 -

Description of Preferred Embodiment(s)

The 3GPP specifications [1] referred to above cover the 'Adjacent Channel Leakage Ratio' (ACLR) for Node Bs specified for equipment that is co-sited with other TDD or FDD Node Bs operating on adjacent channels.

ACLR is a measure of the ratio between the signal power transmitted in the desired channel of operation and the unwanted power transmitted in the channels adjacent to the desired channel. In the referenced version of the specifications, the adjacent channel power is specified as an absolute limit of -80dBm in a measurement bandwidth of 3.84MHz.

This limitation is necessary to ensure that the transmission of a Node B in channel 'A' does not cause unacceptable interference to another Node B receiving in channel 'B' at the same time.

In most Node Bs, the power transmitted in the adjacent channel is determined by the linearity of the power amplifier used in the Node B transmitter. With the PA technology available today, it is not possible to achieve these levels of adjacent channel power at typical Node B transmit powers levels. A brief example follows to illustrate this problem.

It is assumed that the adjacent channel transmissions are related to the level of the 3rd Order intermodulation products generated within the power amplifier. Taking an

02.010-ipw

- 5 -

example Node B power amplifier with P_{1dB} equal to 44dBm, and output IP3 of +63dBm, the maximum wanted transmit power consistent with a -80dBm adjacent channel power will typically be of the order of +15dBm (31mW) for a 5 CDMA (Code Division Multiple Access) test signal. The power amplifier will dissipate close to 100W, representing a DC to RF power conversion efficiency of 0.03%.

10 In order to achieve the specified ACLR, it is clear from the above analysis that, for reasonable transmit powers, a narrow band RF filter is required.

15 Using the power amplifier in the previous example as a reference, the typical ACLR expected at a transmit power of +34dBm (2.5W) will be of the order of 55dB (or -21dBm absolute). With this level of adjacent channel power generated in the PA, the RF Filter is required to provide at least 60dB of protection to the adjacent channel.

20 This is also an extremely difficult specification to meet and requires the use of very high Q dielectric resonators.

25 In the considered base station transmitter the RF filter must immediately follow the power amplifier, and as such the filter must be realised using analogue techniques. It is well known that analogue filters generally exhibit non-constant group delay, although it is possible to 30 approximate constant group delay at the expense of a significantly relaxed roll-off rate. However, for co-

02.010-ipw

- 6 -

siting purposes, a very steep roll-off rate is essential, resulting in conflicting requirements between pass-band group delay variation and rate of filter roll-off.

- 5 Non-constant group delay has a direct effect on the quality of the transmitted signal, as the different frequency components within the signal experience different delays as they pass through the filter. The result is that the RF filter introduces inter-symbol-
10 interference (ISI) to the transmitted signal.

The technical specifications in [1] define 'Error Vector Magnitude' (EVM) as a measure of transmit accuracy. The EVM is a ratio of the ideal received signal compared to
15 the actual received signal, expressed as a percentage. The reference signal is filtered twice by a 'square-root raised-cosine' (RRC) filter, once in the transmitter and once in the measuring receiver; therefore, provided that there are no signal impairments, the received reference
20 signal should be ISI free. It may be noted that the receiver timing is optimised to minimise the EVM.

Simulations of the EVM obtained with the single-channel RF filter present have shown that the EVM is typically
25 17%. These examples only consider the EVM contribution of the RF Filter, the rest of the transmit line up to this point is not included in this calculation.

The 3GPP specifications [1] specify the maximum EVM to be
30 12.5%; therefore it is clear that, even though the

02.010-ipw

- 7 -

presence of the RF filter is required by one part of the specification, it causes a failure in the EVM part.

Although RF analogue equalisation is possible, this 5 approach is not preferred for a number of reasons such as increased size, cost, insertion loss and complexity. Also, the analogue equalisation is optimised for the centre frequency of the filter, and it will be seen below that it is instead beneficial to optimise the 10 equalisation depending on the exact channel centre required by the application.

In order to achieve a suitable analogue equalisation performance in order to obtain an acceptable EVM 15 contribution from the RF filter, the passive equaliser will, typically, be almost as complex as the actual filter itself.

Another problem with the passive RF analogue equalisation 20 approach is that it is not suitable for realising amplitude equalisations without increasing the insertion loss across the whole band of the filter. This is because passive implementations can only create attenuation, not gain.

25 Having ruled out passive RF equalisation, the designer is left with the possibility of baseband equalisation. This can be accomplished either with passive or active analogue baseband filters or with a digital filter. As 30 will be explained in greater detail below, in the preferred embodiment of the present invention, the

02.010-ipw

- 8 -

digital filter solution is the only suitable solution in an application where the equalisation required will need to be optimised for each individual filter and where it will not be possible to tune analogue equalisers in a

5 production environment. The digital equalising filter can be calculated by a computer program directly from a measurement of the RF filter to be equalised, thus having minimal impact on the production of the unit.

10 The channel centre frequencies in the UMTS radio interface are defined to be an integer multiple of 200KHz, however the 5MHz channel allocations are nominally defined between integer blocks of 5MHz, e.g., 1900MHz to 1905MHz. Obviously, the true centre frequency 15 is 1902.5MHz in this example, which is not an integer multiple of 200KHz. The exact choice of the centre frequency is up to the operator or the licensing authority. For the example channel allocation, two possible centre frequencies are possible: 1902.4MHz or 20 1902.6MHz.

As RF filters are expensive and have long lead-time for supply, it is not desirable to have two different filters for each 5MHz block of spectrum. It is far more 25 preferable to keep one filter in stock with the centre frequency at the true centre frequency. The consequence of doing this is that this centred filter will further degrade the transmit accuracy of signals centred on 1902.4MHz and 1902.6MHz.

02.010-ipw

- 9 -

As will be explained below, in a preferred embodiment of the present invention, the amplitude equalising section of the digital pre-equaliser can be easily set up in manufacture to be optimised for the specific centre frequency. Also, if required, the coefficients can be changed remotely when the unit is in the field via software control to enable the unit to transmit on either of the channel centres.

10 Referring now to FIG. 1, a transmitter architecture 100 is designed for use in Node B equipment 200 of a TDD UMTS system (not shown). The Node B equipment is suitable for co-siting. It will be understood that co-siting covers:

15

- a single antenna shared between TDD and FDD base stations
- a single antenna shared between TDD and TDD base stations
- each base station having its own antenna, but multiple base station antennas occupying the same tower at the same cell site.

20

As will be explained in greater detail below, the transmitter architecture 100 incorporates digital pre-equalisation utilising the present invention. The 25 transmit architecture 100 includes a transmit filter section 110, a digital pre-equaliser section 120, a digital-to-analog converter (DAC) section 130, a transmitter section 140, and a post-conversion RF single channel filter section 150. I (In-phase) and Q 30 (Quadrature-phase) components of a modulated transmit signal are applied to respective root-raised-cosine (RRC)

02.010-ipw

- 10 -

filters 112 and 114 in the transmit filter section 110; the RRC filters 112 and 114 have real filter coefficients. The outputs of the RRC filters 112 and 114 are applied to a series arrangement of first FIR (Finite Impulse Response) digital filter 122 and a second FIR digital filter 124; the FIR digital filters 122 and 124, which have complex filter coefficients, will be described in greater detail below. The I and Q outputs from the second FIR digital filter 124 are applied to respective analog-to-digital filters 132 and 134. The outputs of the ADC converters 132 and 134 are applied to a transmit up-converter 142 to produce a single transmit output signal of upwardly-translated frequency. The output of the transmit up-converter 142 is applied to an RF single-channel filter 152, to produce an accurate and highly band-limited transmit output signal T.

The function of the digital pre-equaliser section 120 is to correct for the non-ideal passband characteristics of the single-channel RF filter 152. These non-ideal characteristics can be resolved into two separate factors:

- Non-constant group-delay, which is equivalent to a non-linear phase vs. frequency response; group delay variation is a consequence of designing the filter with a very steep transition region using a reasonable number of sections, and
- Premature roll-off in the pass band of the signal - a consequence of the filter design and practical realisation, i.e., a consequence of finite Q.

02.010-ipw

- 11 -

Each factor can be considered individually.

Group delay equalisation is achieved by making use of the knowledge that a filter with symmetrical impulse response 5 has the property of linear phase. Consequently, the FIR digital filter 122 is constructed to provide group-delay equalisation by filtering the signal with a time-reversed version of the impulse response of the RF filter 152. The impulse response is obtained by applying the inverse 10 discrete Fourier transform on the measured frequency response of the RF filter 152.

A suitable equaliser is obtained by truncating and quantising the impulse response. All the necessary 15 processing can be readily computed by a typical desktop computer. It will be understood that the exact signal processing scheme applied in order to correct the phase response of the filter is not critical, and a suitable signal processing scheme will be within the knowledge of 20 a person of ordinary skill in the field of the invention.

FIG. 2 shows a graphical representation of the magnitude response of a single-channel, narrow-band RF filter used in a 1.9GHz UTRA Node B.

25 FIG. 3 shows the improvement in group delay by pre-filtering the transmitted data signal with the digital FIR filter 122, the upper and lower lines indicating the group delay with and without the pre-filtering 30 respectively.

02.010-ipw

- 12 -

Although the phase-equalising filter 122 provides sufficient correction for the non-linear phase response of the RF filter, the resulting improvement in transmit accuracy (Error Vector Magnitude) becomes limited by the 5 amplitude roll-off in the pass-band. Therefore, it is necessary to introduce a correction for the amplitude response.

For this reason the second FIR filter 124 is used, which 10 attempts a correction for amplitude response without impacting the phase correction properties of the first FIR filter 122. This criterion implies that the second, amplitude-correcting filter 124 must be a symmetrical FIR filter and thus exhibit linear phase.

15 This second filter 124 can also be used to make additional corrections, by correcting for asymmetrical RF filter response around the desired RF channel centre frequency, thus allowing one RF Filter to be optimised 20 for centres offset from the true RF filter centre frequency by a small amount.

In the present example a single RF filter, centred on say 1902.5MHz, can be optimised separately for channel centre 25 frequencies of 1902.4MHz and 1902.6MHz, thus reducing the number of alternative RF filter solutions required.

Being digital, the equalising filter 122 is programmable, providing the ability to optimise the filter response to 30 permit a Node B to operate on either of these two

02.010-ipw

- 13 -

frequencies in the field (e.g., via software control), without the requirement to change the RF filter.

It should also be noted that the phase-equalising filter 5 122 also emphasises the amplitude roll-off of the RF filter as the signal is effectively filtered twice; therefore, the inclusion of the phase equaliser 122 increases the need for the amplitude equaliser 124.

10 FIG. 4 compares the amplitude response of the ideal modulation mask (narrower shape) and the RF filter (wider shape). It may be noted that a 100KHz offset exists between the RF filter and the modulation, resulting in more attenuation on the low side of the modulation.

15 Even though the amplitude roll-off is small (typically the RF filter has rolled off by 1dB at the 3dB points on the modulation mask) the effect on EVM is significant. Characterization of several RF filters has shown that 20 applying only phase equalization results in EVMS around 8% (reduced from 17% for the un-equalized filter). Applying amplitude equalization can improve the error vector to an acceptable level of approximately 3%.

25 To design the FIR filter 124, a 'least-squares' filter design program is used. Such a design program is readily available in commercial software, and need not be described in further detail. As the required equalization response is asymmetrical and most commercial FIR filter 30 design tools produce real-valued symmetrical FIR structures, the filter is designed as a pass-band filter

02.010-ipw

- 14 -

which is then down-converted to a complex low-pass structure suitable for implementation in the transmit digital processing.

5 FIG. 5 shows an example of the result obtained from the amplitude equalizer, the lower and upper lines indicating respectively filter response after phase equalisation only and after both amplitude and phase equalisation. As can be seen by comparing the two lines, a significant
10 improvement in the pass-band flatness is achieved.

The overall filter is simply obtained by convolving the impulse response from the phase equaliser and amplitude equaliser. The length of the filter is optimised by
15 selecting the N consecutive coefficients that contain the highest accumulated energy. The number of taps, N , required is a function of the required equalisation accuracy.

20 It will be understood that in the digitally pre-equalised transmitter architecture 100 described above and shown shown in FIG. 1, because the digital FIR filtering 120 equalises for errors in the analogue single-channel RF filter 152, the RF filter 152 may be deliberately
25 designed to roll-off in the pass-band of the desired signal in order to achieve a specified stop-band attenuation for a smaller, cheaper RF filter implementation.

30 It will be understood that in the digitally pre-equalised transmitter architecture 100 described above and shown

02.010-ipw

- 15 -

shown in FIG. 1, the single-channel RF filter 152 is the final component in the transmit architecture. In a TDD system it is possible to use the RF filter 152 for both transmit and receive functions. Although the receive 5 signal processing is not shown, the digital equaliser can also be used this context.

Unlike the fixed definition transmit filter (root-raised-cosine with roll-off factor of 0.22 in the case of UMTS), 10 the digital pre-equaliser needs to be fully programmable; therefore its associated implementation complexity is high in terms of its gate-count. Therefore, in practice, steps need to be taken to reduce the number of gates required.

15 The number of gates required to build each FIR filter are related to the length of the filter (i.e., number of coefficients) and the quantisation of both the data path and coefficient values. Simulations can be used to 20 determine the optimum filter length and coefficient quantisation required based on a sample of RF filters.

By way of an example the response of several filters was measured and the appropriate equalisers were designed.

25 It was found that the impulse responses for all the filters tested was similar, therefore the size of the multipliers used to implement this filter could be optimised based on the magnitude of the expected value of 30 the coefficients.

02.010-ipw

- 16 -

FIG. 6 shows an implementation of the digital pre-equaliser section 120 based on the above example. As shown, the FIR digital filter (the overall filter, referred to above, obtained by convolving the impulse response from the phase equaliser 122 and amplitude equaliser 124) consists of 40 stages (of which six are shown). Each stage receives a 10-bit real value $Re\{x(n)\}$ and a 10-bit imaginary value $(Im\{x(n)\})$ representing the I/Q input signal $x(n)$ to be filtered, and multiplies (in one of the multipliers 160) this received pair of values by a pair of values representing the real part $Re\{h_{eq}\}$ and the imaginary part $Im\{h_{eq}\}$ of a respective filter coefficient. The imaginary parts of the filter coefficients 1-40 are 5-bit values; the real parts of coefficients 1-15 are 5-bit values, the real parts of coefficients 19-25 are 6-bit values, and the real parts of coefficients 26-40 are 7-bit values. Consequently, the multiplier outputs of the stages produce pairs of 15-bit values (stages 1-15), 16-bit values (stages 16-25), and 17-bit values (stages 26-40). The outputs of the multipliers 160 are combined in summer 170, whose output of a pair of 22-bit values is applied to a bit-select unit 180, which produces a pair of 10-bit output values representing the filtered I/Q signal.

25

It will be understood that the exact number of bits used in the filter is not important, but that it is desirable for the number of bits used in the filter to be optimised to reduce the complexity.

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02.010-ipw

- 17 -

It will be appreciated that, in the application of the present example, the coefficients of the équalisér filter are complex rather than real-only, and that as a consequence the filter is more complex. A filter with 5 complex input data and real-only coefficients has to implement the same filtering on both real and imaginary input data; hence two multiplications are required for each I and Q data pair. If the coefficients are complex, then each multiplier has to be a full complex multiplier 10 which results in four complex multiplications and two summations for each I and Q data pair. Therefore it is a significant benefit in terms of the complexity of the filter if the number of bits in the multipliers are minimised. In this application, the degrees of asymmetry 15 in the filter response are small; therefore the filter coefficients can be optimised so that the largest coefficients are real. This allows the imaginary coefficients to be small, and hence require a fewer number of bits. Also, only one part of the impulse response of 20 the filter has coefficients with large magnitude; therefore the size of the programmable filter can be optimised to the general form expected for the equaliser response. It may be noted that each RF filter may require a slightly different impulse response, and the number of 25 bits assigned to each section of the filter must take this variance into account..

It will thus be understood that there is an advantage to be had for a filter implementation which implements an 30 asymmetrical amplitude and phase response by using a complex-coefficient filter and in which the coefficients

02.010-ipw

- 18 -

themselves have been phase rotated to ensure that the largest coefficients are real; hence for relatively small amounts of filter asymmetry, the implementation complexity of the filter is minimised.

5

It will be understood that the digitally pre-equalised RF filtering scheme described above provides the following advantages:

- it enables 3GPP Node B co-location specifications to be met while providing both good transmit accuracy and acceptable ISI performance; and
- it allows filter centre frequency to be field-tuned in software, permitting a basic RF single-channel filter to be used with its centre frequency being field adjustable to a desired value centred on a UMTS channel.

02.010-ipw

- 19 -

Claims

1. A filter arrangement for use in a wireless communication transmitter, the arrangement comprising:
 - 5 means for receiving digital signals to be transmitted;
 - DAC means for converting the digital signals to analog signals;
 - 10 analogue channel filter means for filtering the analog signals; and
 - 15 digital pre-equaliser filter means coupled before the DAC means for filtering the digital signals, the digital pre-equaliser filter means being adapted to substantially correct for non-ideality in the analogue channel filter means.
2. The filter arrangement of claim 1 wherein the pre-equaliser digital filter means comprises:
 - means for substantially correcting for non-linear phase
 - 20 response in the analogue channel filter means; and
 - means for substantially correcting for amplitude error response in the analogue channel filter means.
3. The filter arrangement of claim 1 or 2 wherein the pre-equaliser digital filter means comprises a finite impulse response (FIR) filter.
4. The filter arrangement of claim 1, 2 or 3 wherein the analogue channel filter means comprises a narrow band
- 30 RF filter.

02.010-ipw

- 20 -

5. The filter arrangement of any preceding claim further comprising up-converter means coupled between the DAC converter means and the analogue channel filter means for providing upward frequency translation.

5

6. The filter arrangement of any preceding claim wherein the digital pre-equaliser filter means is adapted to adjust to a desired value the centre frequency of the analogue channel filter means.

10

7. The filter arrangement of any preceding claim wherein the digital pre-equaliser filter means is programmable.

15

8. The arrangement of any preceding claim wherein the digital pre-equaliser filter means has complex coefficients to provide asymmetric equalisation.

20

9. The arrangement of claim 8 wherein the largest of the filter coefficients are real.

25

10. The arrangement of any preceding claim wherein the analogue channel filter means has roll-off in the pass-band of the desired signal to achieve a specified stop-band attenuation.

11. The arrangement of any preceding claim wherein the arrangement is adapted for use in a received signal path.

02.010-ipw

- 21 -

12. The filter arrangement of any preceding claim
wherein the wireless communication system is a UMTS
wireless communication system.

5 13. The filter arrangement of any preceding claim
wherein the arrangement is adapted for use in a TDD
wireless communication system.

14. Node B equipment comprising the filter arrangement
10 of any preceding claim.

02.010-ipw

- 22 -

15. A method for filtering in a wireless communication transmitter, the method comprising:

receiving digital signals to be transmitted;

providing DAC means converting the digital signals to analog signals;

providing analogue channel filter means filtering the analog signals; and

providing digital pre-equaliser filter means coupled before the DAC means to filter the digital signals,

10 the digital pre-equaliser filter means substantially correcting for non-ideality in the analogue channel filter means.

16. The method of claim 15 wherein the pre-equaliser

15 digital filter means:

substantially corrects for non-linear phase response in the analogue channel filter means; and

substantially corrects for amplitude error response in the analogue channel filter means.

20

17. The method of claim 15 or 16 wherein the pre-equaliser digital filter means comprises a finite impulse response (FIR) filter.

25 18. The method of claim 15, 16 or 17 wherein the analogue channel filter means comprises a narrow band RF filter.

30 19. The method of any one of claims 15-18 further comprising providing up-converter means coupled between

02.010-ipw

- 23 -

the DAC converter means and the analogue channel filter means to provide upward frequency translation.

20. The method of any one of claims 15-19 wherein the 5 digital pre-equaliser filter means adjusts to a desired value the centre frequency of the analogue channel filter means.

21. The method of any one of claims 15-20 wherein the 10 digital pre-equaliser filter means is programmable.

22. The method of any one of claims 15-21 wherein the digital pre-equaliser filter means has complex coefficients to provide asymmetric equalisation.

15 23. The method of claim 22 wherein the largest of the filter coefficients are real.

24. The method of any one of claims 15-23 wherein the 20 analogue channel filter means has roll-off in the pass-band of the desired signal to achieve a specified stop-band attenuation.

25. The method of any one of claims 15-24 further comprising using the DAC means, the analogue channel filter means and the digital pre-equaliser filter means in a received signal path.

26. The method of any one of claims 15-25 wherein the 30 wireless communication system is a UMTS wireless communication system.

02.010-ipw

- 24 -

27. The method of claim 26 wherein the method is performed in Node B equipment in the UMTS wireless communication system.

5

28. The method of any one of claims 15-27 wherein the wireless communication system is a TDD wireless communication system.

10 29. The method of any one of claims 15-28 wherein the step of providing the digital pre-equaliser filter means includes:

performing measurements of the analogue channel filter means, and

15 automatically calculating on the basis of the measurements coefficients of the digital pre-equaliser filter means.

20 30. The method of any one of claims 15-28 wherein the step of providing the digital pre-equaliser filter means includes:

25 providing quantised filter coefficients of the digital pre-equaliser filter means based on the impulse response of the digital pre-equaliser filter means.

31. A filter arrangement, for use in a wireless communication transmitter, substantially as hereinbefore described with reference to the accompanying drawings.

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02.010-ipw

- 25 -

32. A method, for filtering in a wireless communication transmitter, substantially as hereinbefore described with reference to the accompanying drawings.

02.010-ipw

- 26 -

AbstractARRANGEMENT AND METHOD FOR RF FILTER

5 An arrangement (100) and method for RF filtering in a Node B of a UMTS TDD system by providing: a DAC converter (130) converting digital signals to analog signals; providing a narrow band analogue channel filter (150) filtering the analog signals; and providing a digital 10 pre-equaliser FIR filter (120) coupled before the DAC (120) to filter the digital signals, the digital pre-equaliser filter means substantially correcting for non-linear phase response (122) non-ideality and amplitude response non-ideality (124) in the analogue channel 15 filter (150).

This provides the following advantage(s):

it enables 3GPP Node B co-location specifications to be met while providing both good transmit accuracy and 20 acceptable ISI performance; and it allows filter centre frequency to be field tuned in software, permitting a basic RF single-channel filter to be used with its centre frequency being field adjustable to a desired value centred on a UMTS channel.

25

(FIG. 1 to accompany abstract)

02.010-ipw

1/6

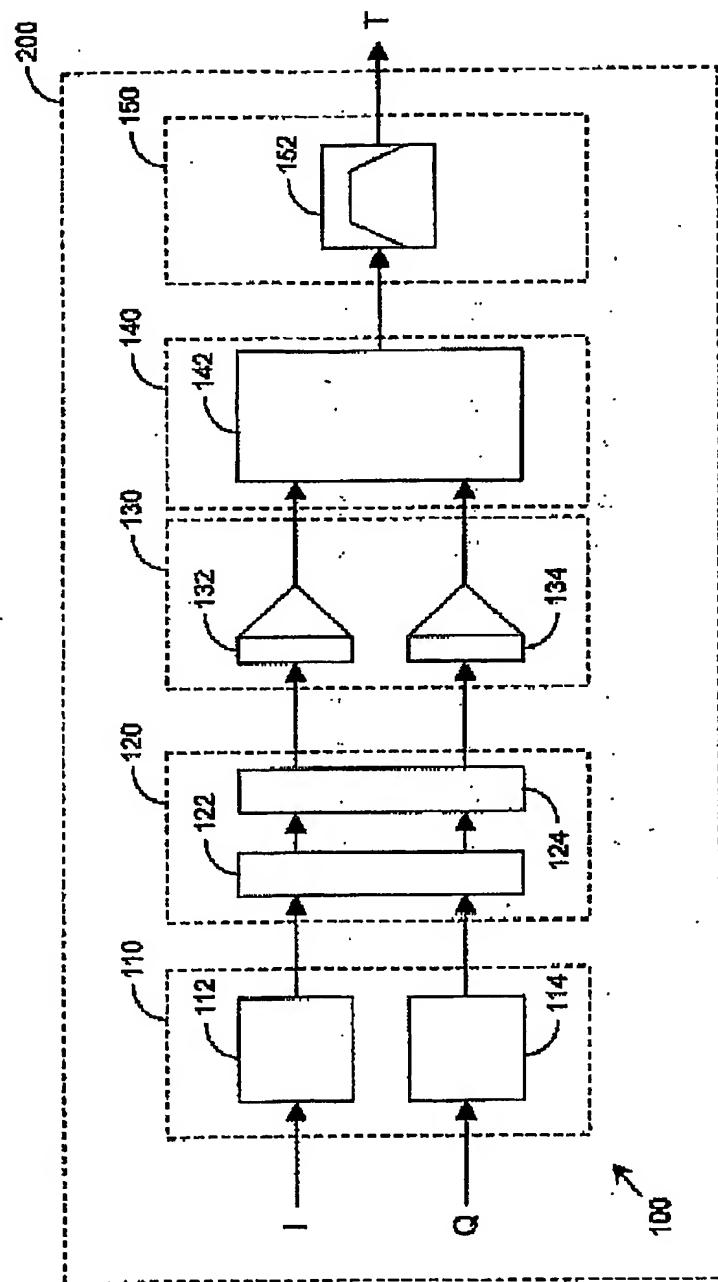


FIG. 1

02.010-ipw

2/6

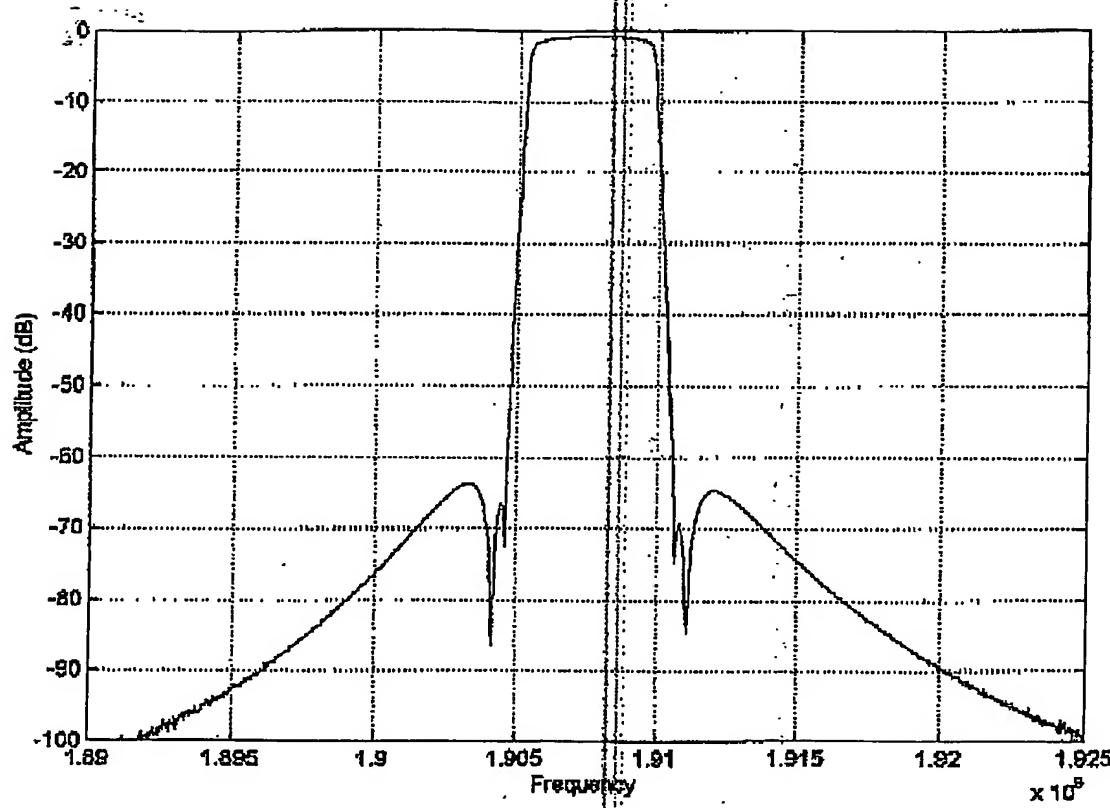


FIG. 2

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3/6

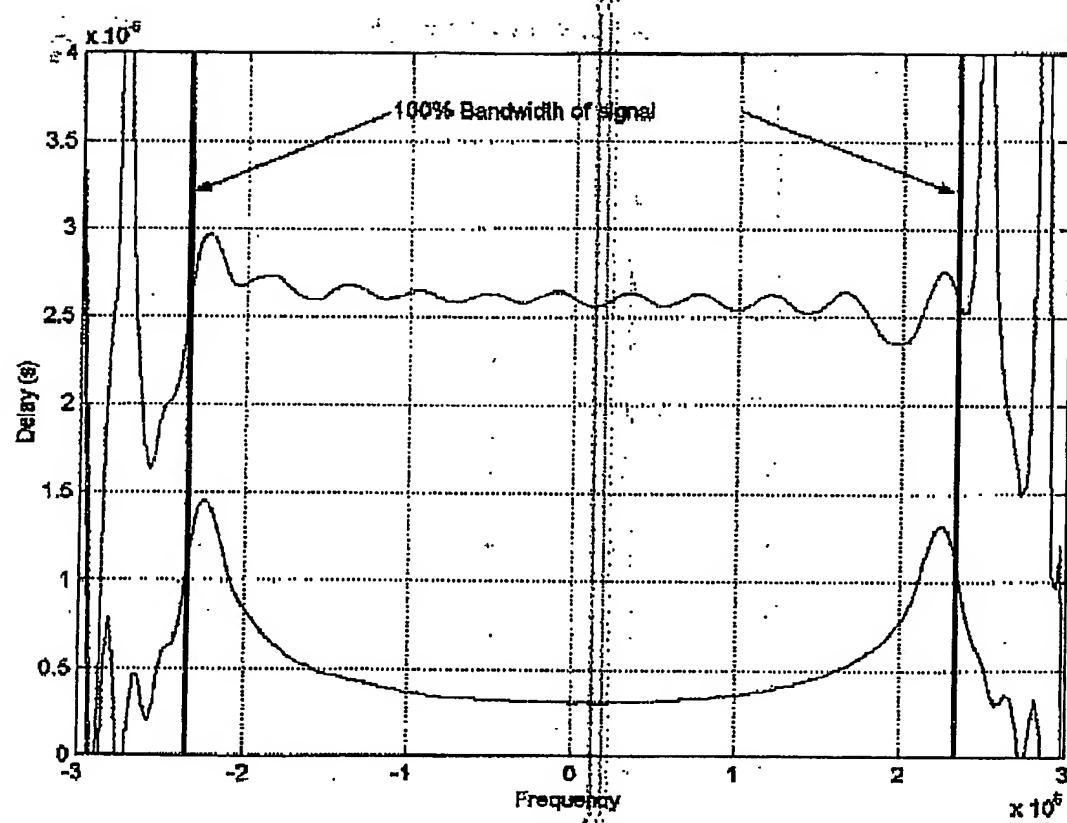


FIG. 3

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4/6

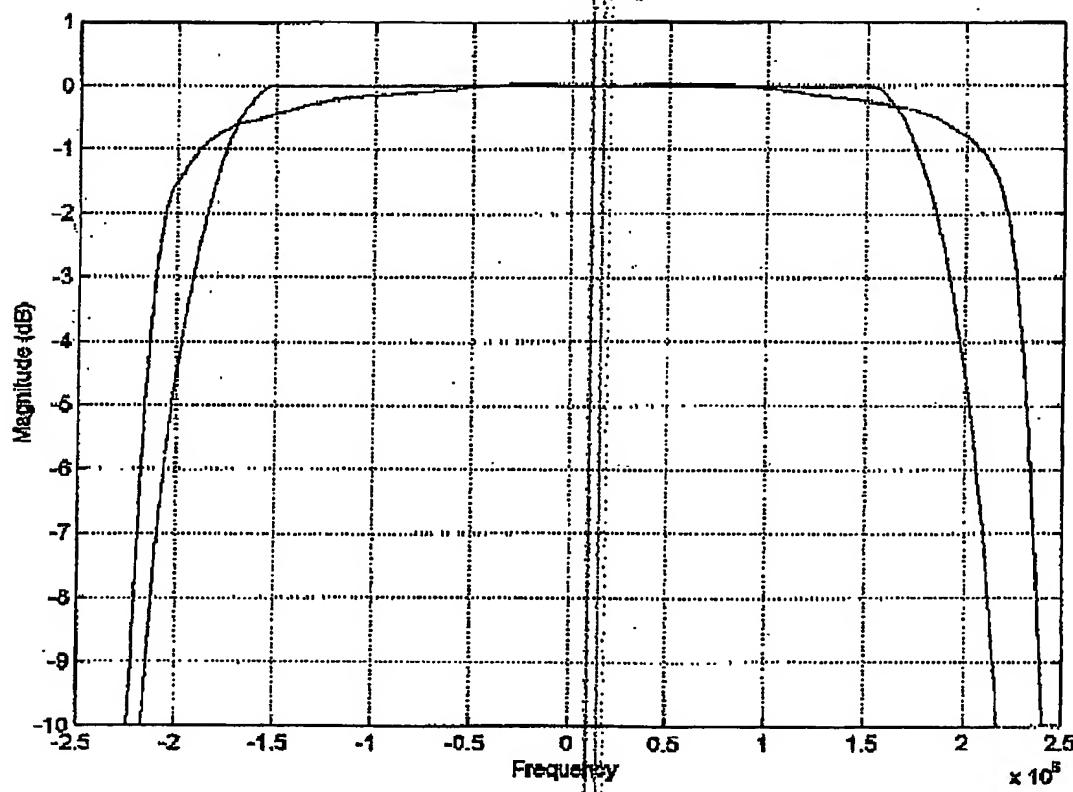


FIG. 4

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5/6

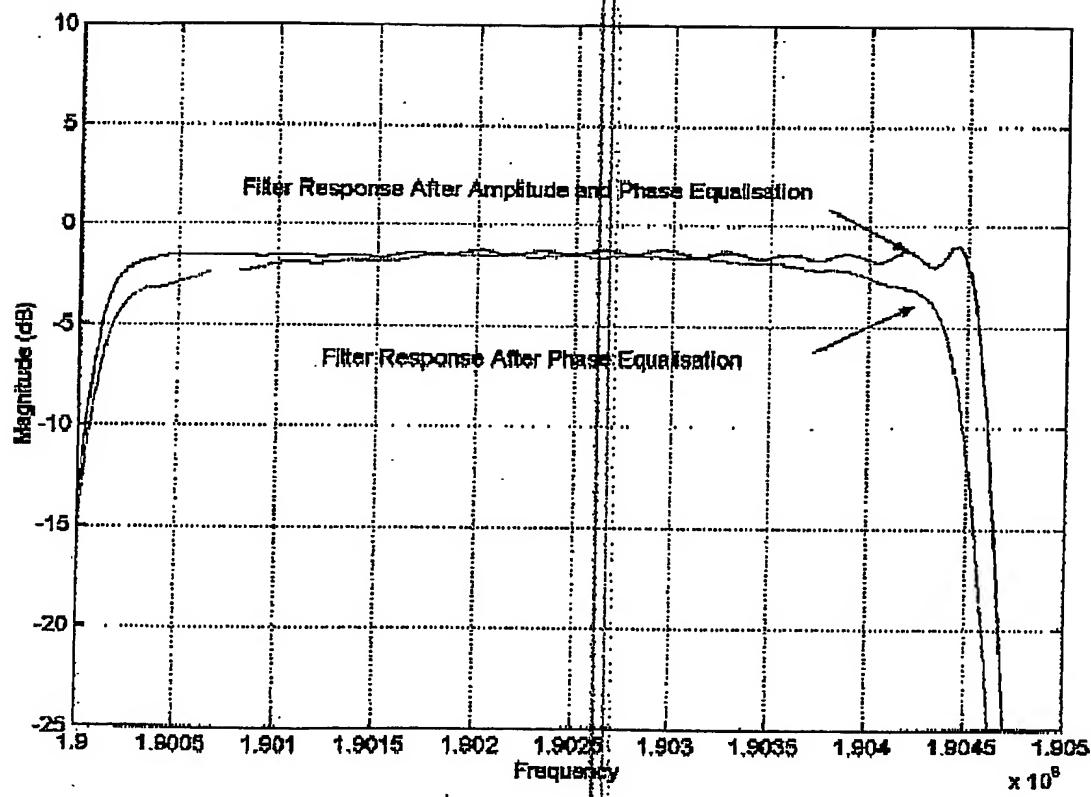


FIG. 5

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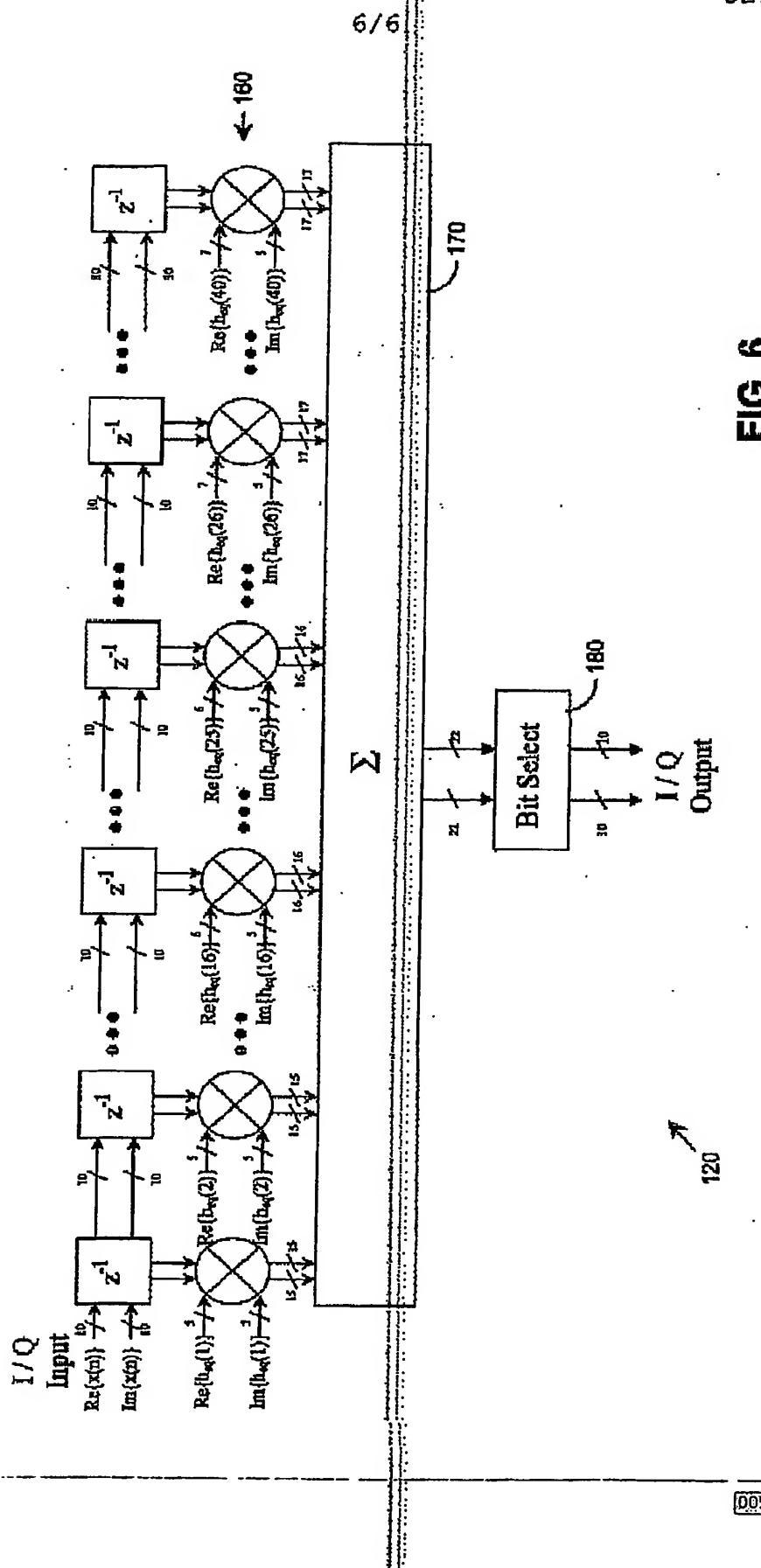
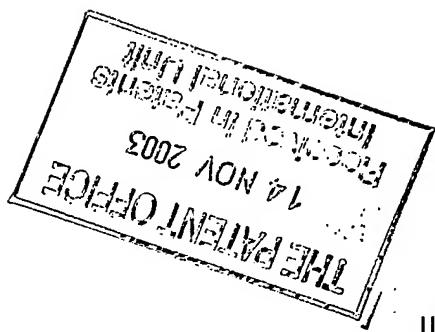


FIG. 6

PCT/GB/2003 004506

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PCT Application
GB0304506

